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FROM	Gerald Chan gerald.chan@bingham.com	(650) 849-4800	(650) 849-4870
PAGES	(including this cover page): 8		

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650.849.4800 fax

RE Request for a Corrected Filing Receipt  
U. S. Application No. 10/656,801  
Filing Date: September 4, 2003  
Art Unit: 2825  
Atty. Docket No.: CA7038422001

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AUG 01 2006

PATENT

Docket No. CA7038422001

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*Cynthia K. Dawn*  
Cynthia K. Dawn

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application for:

Manish Pandey et al.

Serial No.: 10/656,801

Filing Date: September 4, 2003

For: **METHOD AND SYSTEM FOR LOGIC  
EQUIVALENCE CHECKING**

Examiner: Not Yet Assigned

Confirmation No.: 7106

Group Art Unit: 2825

**REQUEST FOR A CORRECTED FILING RECEIPT**

Office of Initial Patent Examination's  
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Dear Sir:

Applicant respectfully requests that the Official Filing Receipt for the above-identified patent application, a copy of which is attached with the correction marked, be corrected and a new Updated Filing Receipt be issued. The Filing Receipt contains an error in the "Correspondence Address" section.

Please correct the Filing Receipt to read as follows:

The Correspondence Address should read,

--Customer Number 55498  
**Bingham McCutchen LLP**  
Three Embarcadero Center  
San Francisco, CA 94111-4067--.

PA/52186256.1


PATENT  
Docket No. OI7012185001

Applicants attach a copy of the Change of Correspondence Address dated June 17, 2004, the our Facsimile Cover Sheet dated June 17, 2004, and the Auto-Reply Facsimile Transmission from the U. S. Patent and Trademark Office dated June 17, 2004.

The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 50-2518 (Docket No. CA7038422001).

Respectfully submitted,

Date: August 1, 2006

By:   
Gerald Chan  
Reg. No. 51,541

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/656,801	09/04/2003	2825	1800	CA7038422001	5	70	2

CONFIRMATION NO. 7106

UPDATED FILING RECEIPT



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Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

Manish Pandey, San Jose, CA;  
Yung-Te Lai, Cupertino, CA;  
Bret Siarkowski, Marlborough, MA;  
Kei-Yong Khoo, Berkeley, CA;  
Chih-Chang Lin, San Jose, CA;

## Domestic Priority data as claimed by applicant

This appln claims benefit of 60/453,084 03/07/2003

## Foreign Applications

If Required, Foreign Filing License Granted: 11/26/2003

Projected Publication Date: 09/09/2004

Non-Publication Request: No

Early Publication Request: No

## Title

Method and system for logic equivalence checking

Preliminary Class

716

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1900 University Avenue  
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TO: Commissioner for Patents	703-872-9306	

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FROM: Peter C. Mei (650) 849-4800 (650) 849-4870  
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PAGES: (INCLUDING THIS COVER PAGE): 2

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RE: U.S. Appl. No. 10,656,801

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